

<IGBT Modules>

CM450DX-13T/CM450DXP-13T

HIGH POWER SWITCHING USE INSULATED TYPE



Collector current Ic 4 5 0 A Collector-emitter voltage V_{CES} 6 5 0 V 175°C Maximum junction temperature T_{vjmax}

- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- •Tin-plating pin terminals



Collector current Ic	4 5 0 A
Collector-emitter voltage Vces	6 5 0 V
Maximum junction temperature T _{vjmax}	175 °C

- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

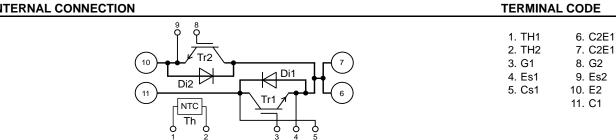
AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- V_{CEsat} selection for parallel connection

INTERNAL CONNECTION



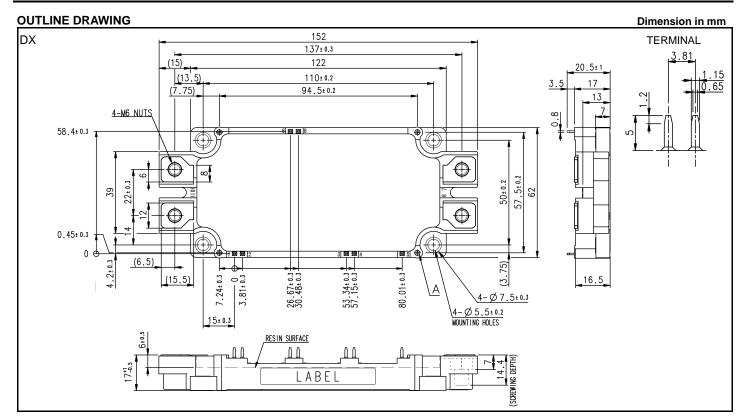
OUTLINE DRAWING Dimension in mm 97 MOUNTING HOLES **SECTION A** COM. Ø2.6 Ø2.32 58 \oplus

1

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INSULATED TYPE

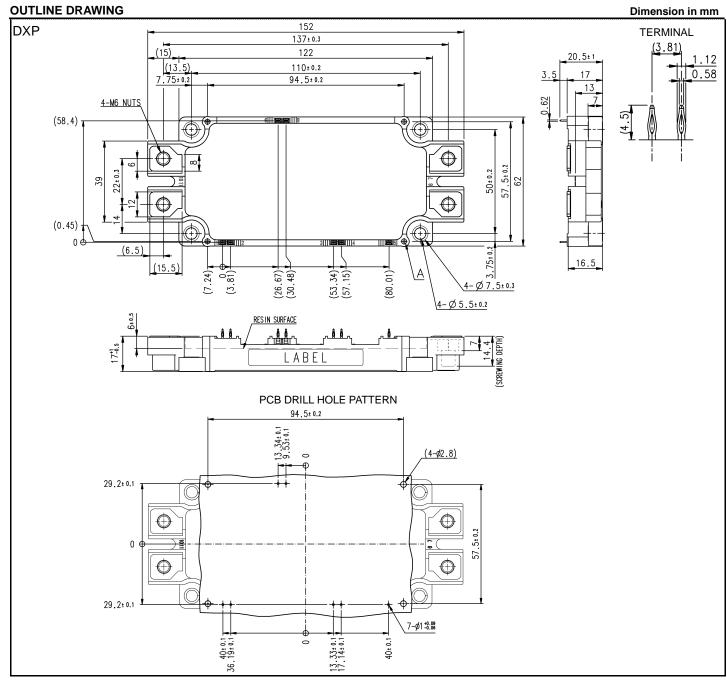


Tolerance otherwise specified

Divisio	Tolerance			
	0.5	to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over	120	to 4	400	±1.2

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INSULATED TYPE



Tolerance otherwise specified

Divisio	n of	Tolerance		
	0.5	to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over	120	to 4	400	±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	650	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =106 °C (Note2, 4)	450	۸
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	900	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1685	W
I _E (Note1)	Emitter current	DC (Note2)	450	۸
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T $_{vj}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Complete	ltore	Conditions			Limits		Unit
Symbol	Item			Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	-	-	1.0	mA	
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =45 mA, V _{CE} =10 V		5.4	6.0	6.6	V
V _{CEsat} (Terminal)		I _C =450 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.45	1.80	
		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.55	-	V
	Callantar are the restriction value of	(Note5)	T _{vj} =150 °C	-	1.60	-	
	Collector-emitter saturation voltage	I _C =450 A,	T _{vj} =25 °C	-	1.30	1.55	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	1.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.35	-	1
Cies	Input capacitance		,		-	60.1	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited	-	-	2.6	nF	
Cres	Reverse transfer capacitance		-	-	1.2	1	
Q _G	Gate charge	V _{CC} =300 V, I _C =450 A, V _{GE} =15 V	-	1.86	-	μC	
t _{d(on)}	Turn-on delay time	V 000 V I 450 A V 45 V	-	-	400		
tr	Rise time	Vcc=300 V, Ic=450 A, V _{GE} =±15 V,	-	-	200	ns	
t _{d(off)}	Turn-off delay time	B 460 Indication land	-	-	400		
tf	Fall time	R _G =1.6 Ω, Inductive load	-	-	400	1	
. (Noted)		I _E =450 A, G-E short-circuited,	T _{vj} =25 °C	-	1.60	2.15	
V _{EC} (Note1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.65	-	V
(Terminal)	Foritten cellecten veltere	(Note5)	T _{vj} =150 °C	-	1.65	-	1
. (Noted)	- Emitter-collector voltage	I _E =450 A,	T _{vj} =25 °C	-	1.45	1.85	
V _{EC} (Note1)		G-E short-circuited,	T _{vj} =125 °C	-	1.50	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.50	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =300 V, I _E =450 A, V _{GE} =±15 V,		-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =1.6 Ω, Inductive load		-	31.5	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =300 V, I _C =I _E =450 A,		-	9.5	-	1
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =1.6 Ω, T _{vj} =150 °C,	-	21.2	-	mJ	
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load	-	17.4	-	mJ	
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =2	5 °C (Note4)	-	0.87	-	mΩ
r _g	Internal gate resistance	Per switch		-	1.3	-	Ω

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HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
		Conditions	Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item				Unit				
		Conditions		Min.	Тур.	Max.	Offic	
$R_{th(j-c)Q}$	Junction to case, per Inverter IGBT (Note4)				-	89	K/kW	
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	131	N/KVV	
В	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7)	-	11.5	-	K/kW	
$R_{th(c-s)}$	Contact thermal resistance	per 1 module,	PC-TIM applied (Note4, 8)	-	3.1	-	TVKVV	

MECHANICAL CHARACTERISTICS

C: made al	ltare	Conditions			I Imit		
Symbol	Item	Cor	Conditions			Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
		Colder pin tupe (DV)	Terminal to terminal	17	-	-	
_	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	mm
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Pressiit pin type (DXP)	Terminal to base plate	16.8	-	-
		Solder pin type (DX)	Terminal to terminal	10	-	-	
_			Terminal to base plate	16.2	-	-	mm
da	Clearance	D (1) (D)(D)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y	On the centerline X, Y (Note9)		-	+200	μm
m	mass	-	-		300	-	g

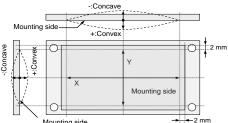
- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}\!\!:$ resistance at absolute temperature T_{25} [K]; $T_{25}\!\!=\!\!25$ [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K)/D_(C-S)=50 μ m.
- 8. Typical value is measured by using PC-TIM of λ =3.4 W/(m·K)/D_(C-S)=50 μ m.
- $9. \ \ The \ base \ plate \ (mounting \ side) \ flatness \ measurement \ points \ (X, \ Y) \ are \ shown \ in \ the \ following \ figure.$



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HIGH POWER SWITCHING USE

INSULATED TYPE

 $Note 10. \ Use the following screws when mounting the printed circuit board (PCB) on the standoffs.$

PCB thickness : t=1.6.

	Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.73 ± 0.073 N·III	

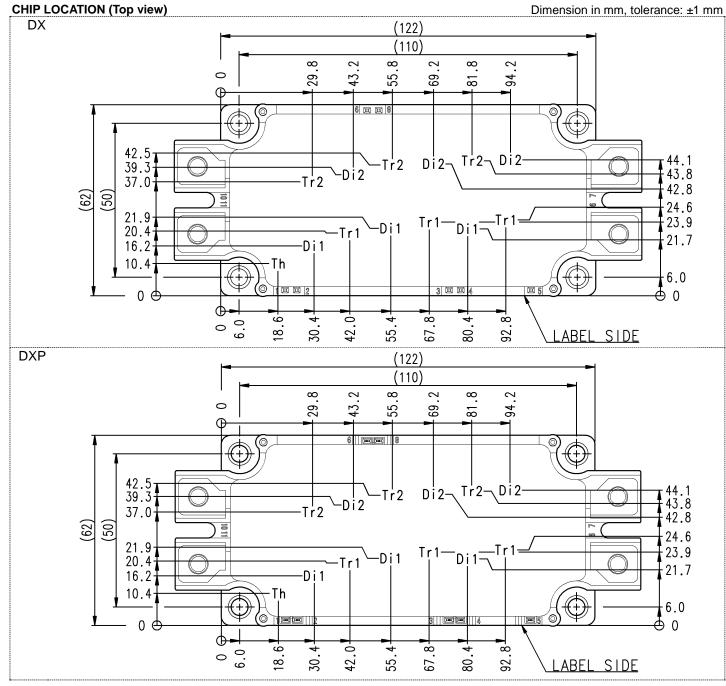
RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Linit		
		Conditions	Min.	Тур.	Max.	Unit
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	=	300	450	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.6	-	16	Ω

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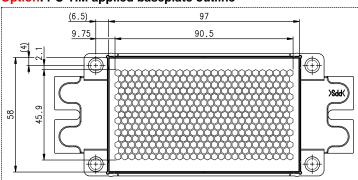
HIGH POWER SWITCHING USE

INSULATED TYPE



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

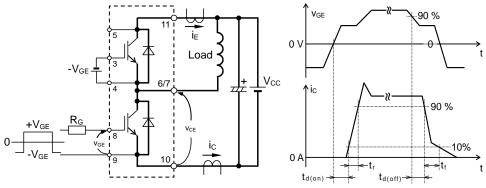
Option: PC-TIM applied baseplate outline

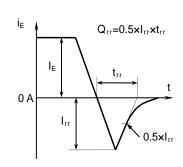


HIGH POWER SWITCHING USE

INSULATED TYPE

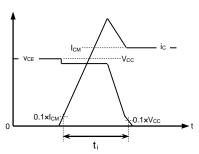
TEST CIRCUIT AND WAVEFORMS

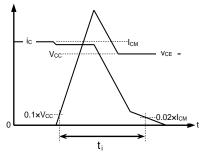


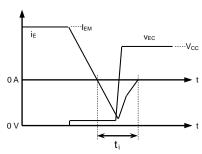


Switching characteristics test circuit and waveforms









IGBT Turn-on switching energy

IGBT Turn-off switching energy

FWD Reverse recovery energy

G-E short

G-E short

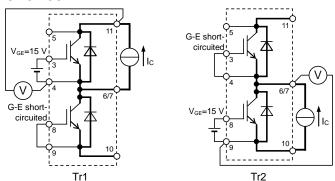
circuited

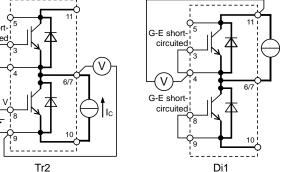
Di2

circuited

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT





V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

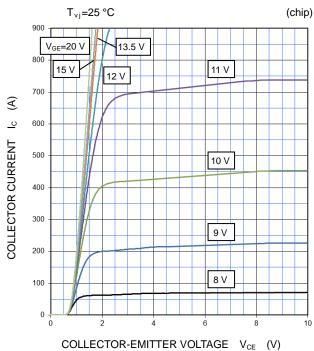
HIGH POWER SWITCHING USE

INSULATED TYPE

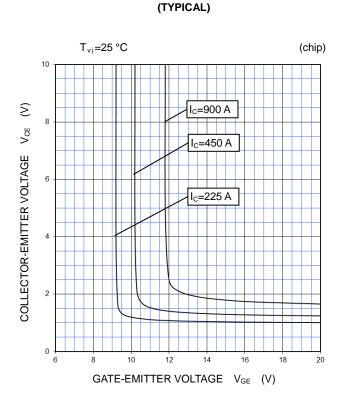
PERFORMANCE CURVES

INVERTER PART

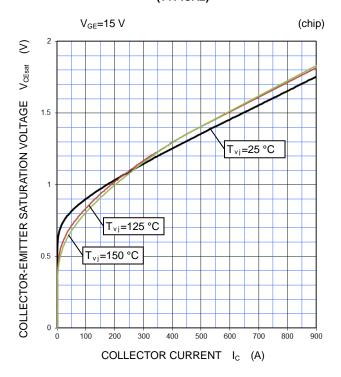
OUTPUT CHARACTERISTICS (TYPICAL)



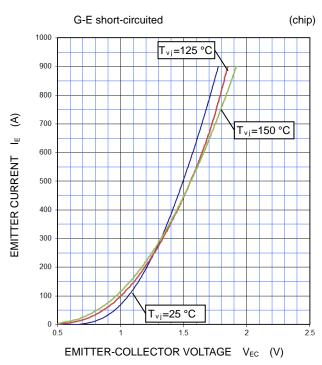
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



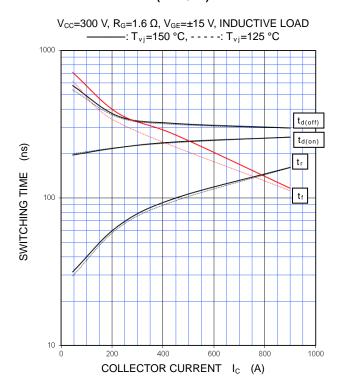
HIGH POWER SWITCHING USE

INSULATED TYPE

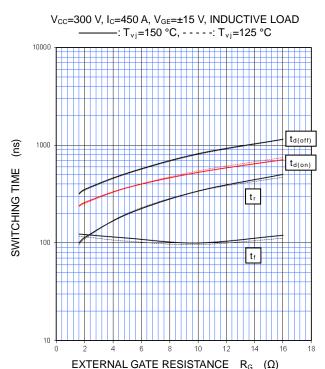
PERFORMANCE CURVES

INVERTER PART

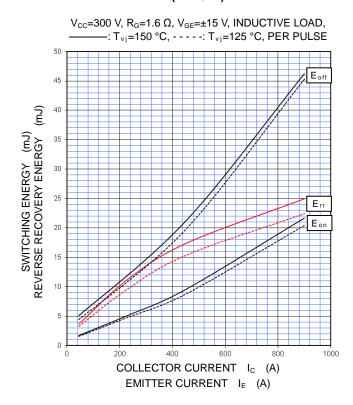
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



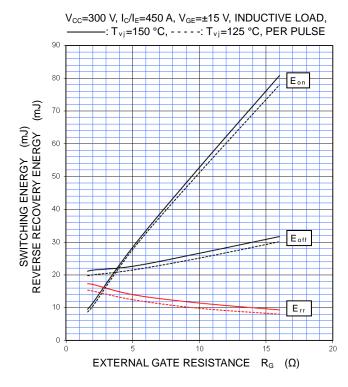
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



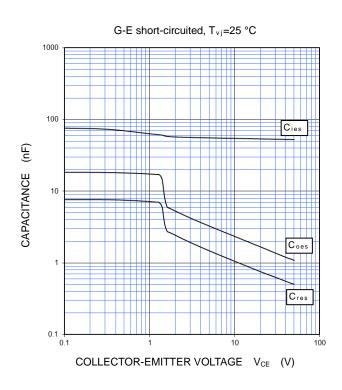
HIGH POWER SWITCHING USE

INSULATED TYPE

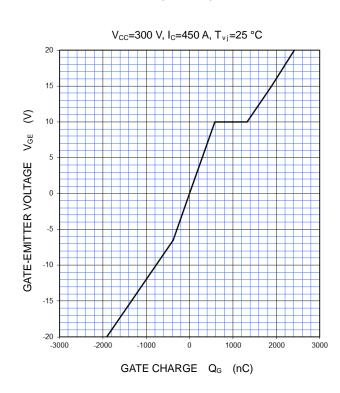
PERFORMANCE CURVES

INVERTER PART

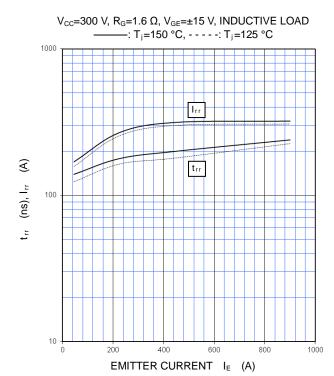
CAPACITANCE CHARACTERISTICS (TYPICAL)



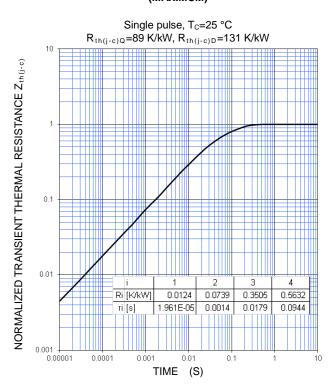
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

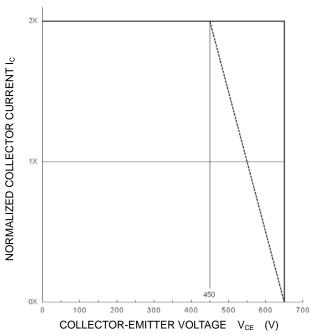
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

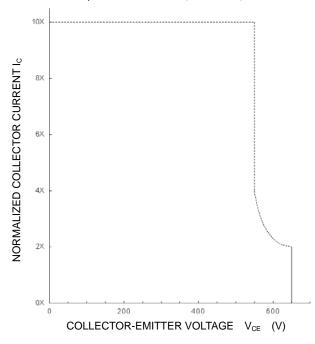
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 V_{CC} \leq 450 V, R_G = 1.6~16 Ω , V_{GE} = ±15 V, - : T_{vj} = 25~150 °C (Normal load operations (Continuous) - - - - - : T_{vj} = 175 °C (Unusual load operations (Limited period)



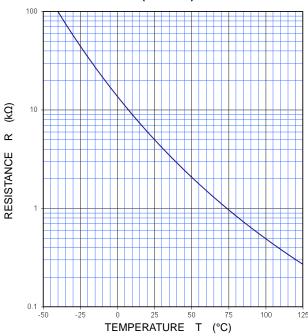
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 400 \text{ V}$, $R_G = 1.6 \sim 16 \Omega$, $V_{GE} = \pm 15 \text{ V}$, $T_{vj} = 25 \sim 150 \text{ °C}$, $t_W \le 8 \mu \text{s}$, Non-Repetitive



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

This product is designed for industrial application purpose. The performance, the quality and support level of the product is guaranteed by "Customer's Std. Spec.".

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